



## APPLICATIONS

- Workstations, servers
- Desktop computers
- DSP applications
- Distributed power architectures
- Telecommunications equipment
- Data communications equipment
- Wireless communications equipment

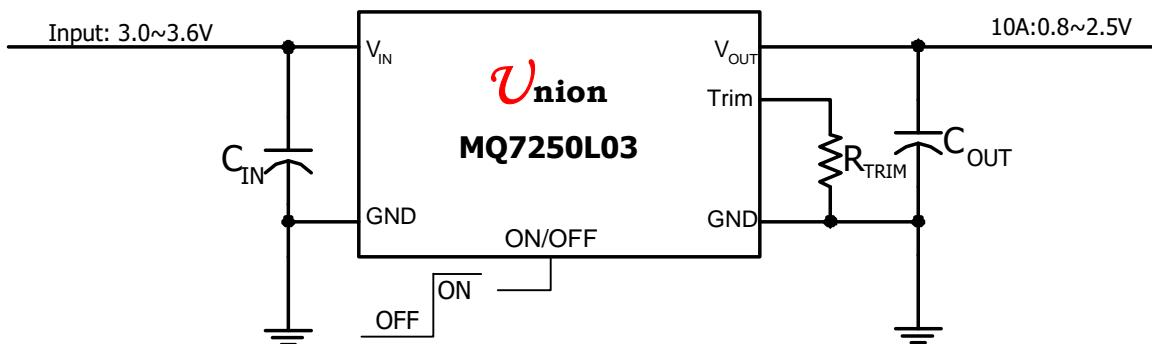
## FEATURES

- 3.0~3.6V input voltage
- Output Voltage:
  - 0.8V~2.5V
- Output Current up to 10A
- Output voltage ripple: 20mV<sub>PP</sub>
- High Efficiency 94%
- Margin-up /Margin-Down
- Remote on/off control – positive
- Over current /short-circuit protection
- Remote Sense
- EasyTrack™
- High reliability: designed to meet 5 million hour MTBF
- Minimal space on PCB:
  - 25.40 mm x 15.75mm x 8.7mm or
  - 1.00 in x 0.62 in x 0.34 in
- Operating Temperature: -40°C to +85°C
- UL/IEC/EN60950 compliant
- RoHS Compliant available
- Remote Control Logic mode
- PoLA Pin Configuration

## Description

The **PoLA MQ7250L03** series Power Modules are non-isolated dc-dc converters that operate over a wide input voltage range of 3.0Vdc to 3.6Vdc and provide a precisely (2%) regulated dc output with industry standard pin configuration. Such a module is suitable to application with unregulated 3.3V power supply bus. The modules have a maximum output current rating of 10A at typical full-load efficiency over 94%. Standard features include remote on/off with positive logic and output voltage adjustment, over-current protection, over-temperature protection EasyTrack control and margin up/down. Option features include through hole or SMT, narrow or wide output trim range.

### \*\*\*\*\* Typical Application Circuit \*\*\*\*\*

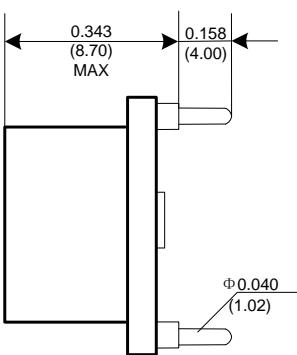
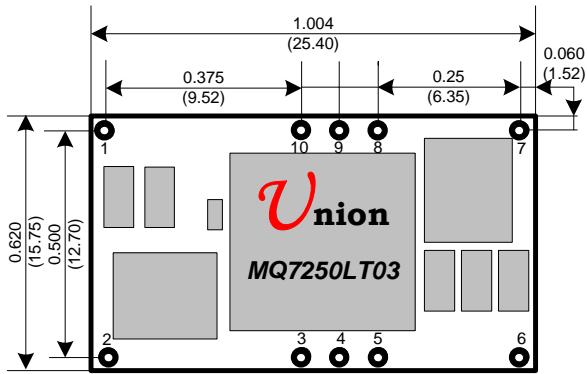


**Performance Specifications (at TA=+25°C)**

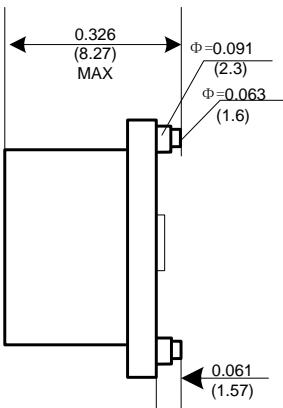
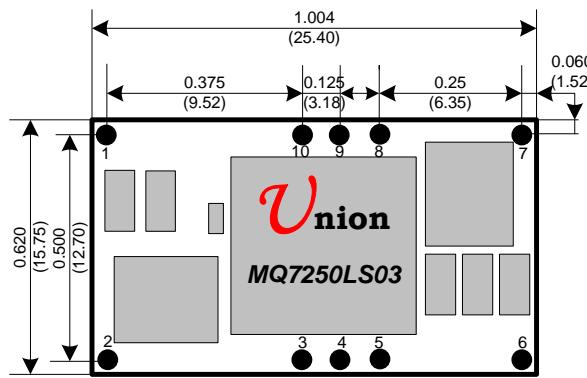
Model	Input V <sub>IN</sub> Range (V)	Output				Efficiency (%)	
		I <sub>OUT</sub> (A)	Trim Range (V)	Regulation			
		Line (%)	Load (%)				
MQ7250LT03	3.0~3.6	10	0.8 ~ 2.5	0.5	0.5	94	

**Mechanical Specifications**

Dimensions are in mm (inches)



Pin	Description
1	GND
2	Vin
3	ON/OFF Control
4	Trim
5	Sense
6	Vout
7	GND
8	EasyTrack™ Control
9	Margin Down
10	Margin Up



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**Ordering Information**

# MQ7250LT03

Union Microsystems  
Power module P/N

POLA Pin

S: SMT Package

T:Through Hole Pakage

Input Voltage Range:

03:3.0~3.6V

05:4.5~5.5V

**Absolute Maximum Ratings**

Note: These are stress ratings. Exposure of devices to any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance Specifications Table is not implied.

Parameter	Symbol	Min	Max	Unit
Input Voltage	V <sub>IN</sub>	-0.3	3.6	V
Storage Temperature	T <sub>STG</sub>	-40	125	°C

**MQ7250L03 Electrical Specifications: ( $T_A=+25^\circ\text{C}$ )**

Parameter	Condition	Symbol	Min	Typ	Max	Unit
Input Voltage Range		V <sub>IN</sub>	3.0		3.6	V
Output Current		I <sub>O</sub>	0		10	A
Output Voltage Set point	100% load	ΔV <sub>O</sub>	-2		+2	%
Temperature Regulation	T <sub>A</sub> = T <sub>A,MIN</sub> To T <sub>A,MAX</sub>	-		0.4		%V <sub>O,SET</sub>
Remote Sense Range					0.5	V
Line Regulation	See each output's corresponding character figure					
Load Regulation						
Output Ripple and Noise Voltage	I <sub>O</sub> =10 A, 0~20MHz (Detail Please see corresponding figure)					
Transient Response						

**General Specifications**

Parameter	Condition	Symbol	Min	Typ	Max	Unit
Maximum Capacitive Load	10A resistive load + Aluminum capacitor			6600		μF
	10A resistive load +Sanyo POSCAP			2000		
Overcurrent Protection				15		A
Output short-circuit current (average)	All				0.8	A
Under Voltage Lockout Trip Level	Rising and falling V <sub>IN</sub> , 3% hysteresis			2.8		V
Logic High (Module OFF)		V <sub>IH</sub>	2.5		V <sub>IN,MAX</sub>	V
Logic Low (Module ON)		V <sub>IL</sub>	-0.7		0.3	V
Start-up Time	10A resistive load, no external output capacitors			2		mS
Switching Frequency		F <sub>O</sub>		300		KHz
Operating Temperature	Natural convection		-40		85	°C
Vibration	3 Axes, 5 Min Each		10~55Hz, 0.35mm, 5g			
	3 Axes, 6 Times Each		Peak Deviation 300g, Settling Time 6mS			
MTBF			5,000,000			Hour

## Test Configurations

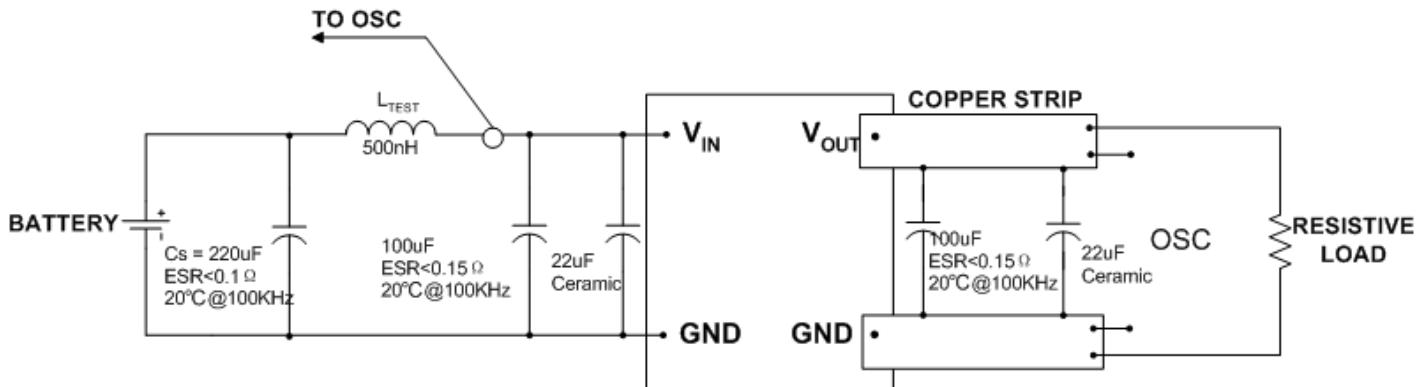


Fig 1 Test setup for input noise, output noise and ripple

Note:

Output noise is measured with  $0.1\mu F$  ceramic capacitor connected at the output. OSC measurement should be made using a BNC socket.

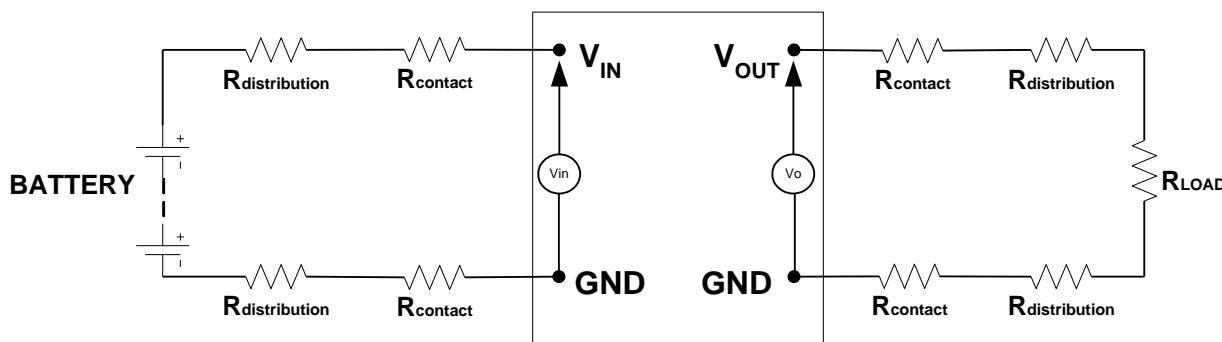


Fig 2 Test setup for efficiency

Note:

All voltage measurements must be taken at the module's terminals, as shown above. If sockets are needed, Kelvin connections are required at the module terminals to avoid measurement errors due to socket contact resistance.

## Technical Notes

### EasyTrack™ Function

The EasyTrack™ function is available with the all “Point-of-Load Alliance” (POLA) products. EasyTrack™ was designed to simplify the amount of circuitry required to make the output voltage from each module power up and power down in sequence. The sequencing of two or more supply voltages during power up is a common requirement for complex mixed-signal applications, that use dual-voltage VLSI ICs such as DSPs, micro-processors, and ASICs.

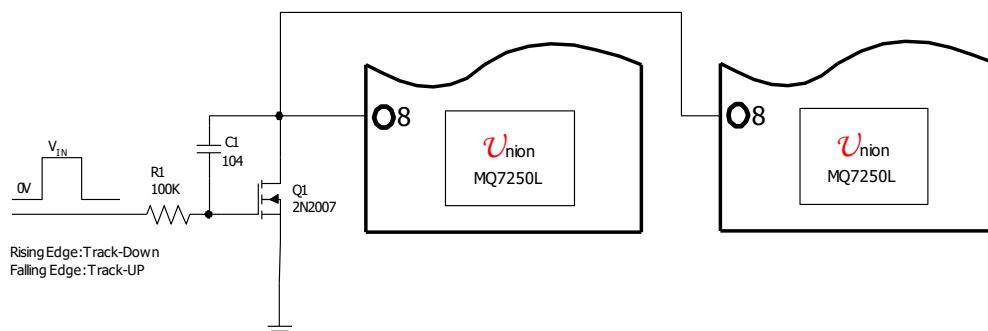


Fig 3 Simultaneous Power Up and Power Down Using EasyTrack™

## How *EasyTrack<sup>TM</sup>* Works

*EasyTrack<sup>TM</sup>* works by forcing the module's output voltage to follow a voltage presented at the *EasyTrack<sup>TM</sup>* control pin. This control range is limited to between 0 V and the module's set-point voltage. Once the *EasyTrack<sup>TM</sup>* control pin voltage is raised above the set-point voltage, the module's output remains at its set-point. As an example, if the *EasyTrack<sup>TM</sup>* control pin of a 2.5-V regulator is at 1 V, the regulated output will be 1 V. But if the voltage at the *EasyTrack<sup>TM</sup>* control pin rises to 3 V, the regulated output will not go higher than 2.5 V. When under *EasyTrack<sup>TM</sup>* control, the regulated output from the module follows the voltage at its *EasyTrack<sup>TM</sup>* control pin on a volt-for-volt basis. By connecting the *EasyTrack<sup>TM</sup>* control pin of a number of these modules together, the output voltages will follow a common signal during power-up and power-down. The control signal can be an externally generated master ramp waveform, or the output voltage from another power supply circuit. For convenience the *EasyTrack<sup>TM</sup>* control incorporates an internal RC charge circuit. This operates off the module's input voltage to provide a suitable rising voltage ramp waveform.

## **Input Voltage Range**

The MQ7250L03 Series can be used in a wide variety of applications, esp. most of unregulated 3.3V intermediate power supply bus system. Its wide input voltage ranges can tolerate worst voltage drop from cheap isolated Brick-type Bus-converter, so it reduces total system cost on power supply.

## **Return Current Paths**

The MQ7250L03 Series are non-isolated DC/DC converters. Their two Common pins (pins 1 and 7) are connected to each other internally. To the extent possible with the intent of minimizing ground loops, input return current should be directed through pin 1 (also referred to as---Input or Input Return), and output return current should be directed through pin 7 (also referred to as---Output or Output Return) as short as possible.

## **I/O Filtering**

All the specifications of the MQ7250L03 Series are tested with specified output capacitors. However, certain input capacitors are necessary to improve the power modules' operating conditions and to reduce the ac impedance. For example, under some conditions, the power modules can't normally start up when fully loaded due to the high ac-impedance input source. External input capacitors serve primarily as energy-storage devices. They should be added close to the input pins of the MQ7250L03 and selected for bulk capacitance (at appropriate frequencies), low ESR, and high rms-ripple-current ratings. All external capacitors should have appropriate voltage ratings. To reduce the amount of ripple current fed back to the input supply (input reflected-ripple current), an external L-C filter can be added with the inductance as close to the power module as possible.

MQ7250L03's output ripple and transient response can be improved with the increasing output capacitance. When using output capacitors, take care that the total output capacitance does not exceed MQ7250L03's Maximum Capacitive Load to avoid issuing the module's over-current protection mechanism in the start-up procedure.

When an external L-C filter is added to reduce ripple on load, for best results, the filter components should be mounted close to the load circuit rather than the power module.

**When testing the relationship between external capacitors and output voltage noise, the oscilloscope's probe should be applied to the module's end directly with scope probe ground less than 10mm in length.**

## **Input Fusing**

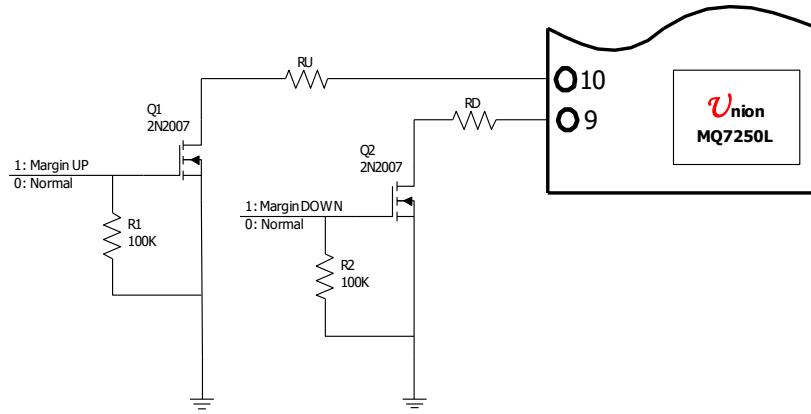
The MQ7250L03 Series is not internally fused. Certain applications and/or safety agencies may require the installation of fuses at the inputs of power conversion components. The selection of the fuses should conform to the following :

1. The fuse value should be selected to be greater than the maximum input current of the modules, which occurs at the minimum input voltage.
2. Use either slow-blow or normal-blow fuses.
3. Both input traces must be capable of carrying a current of 1.5 times the value of the fuse without opening.

## **Margin Up/Down Controls**

The MQ7250L incorporate *Margin Up* and *Margin Down* control inputs which allow the output voltage to be momentarily adjusted, either up or down, by a nominal 5%. This provides a convenient method for dynamically testing the operation of the load circuit over its supply margin or range. It can also be used to verify the function of supply voltage supervisors. Pulling the appropriate margin control input directly to the GND terminal makes the 5% adjustment. Adding series resistors to the control inputs can also accommodate adjustments of less than 5%. Detailed implemented circuit refers to Fig4. The value of the resistor can be selected from Table 1.

If these functions are not been used, just leave these pins float and be care of that Margin up and Margin down cannot be activated simultaneously, connect the ground reference directly to the **Output Return GND** as short as possible.



**Fig4 Margin up and Margin down application circuit**

UP/Down adjust resistor calculation:

$$RU \text{ or } RD = \frac{499}{\Delta\%} - 99.8$$

Resistor values are in kΩ; Δ% is desired amount of margin adjust in percent.

**Table 1 Margin Up/Margin Down Resistor Values**

%Adjust	R <sub>U</sub> /R <sub>D</sub>
1	397.0 kΩ
2	150.0 kΩ
3	66.5 kΩ
4	24.9 kΩ
5	0kΩ

### Safety Considerations

MQ7250L03's are non-isolated DC/DC converters. In general, all DC-DC's must be installed in compliance with relevant safety-agency specifications (usually UL/IEC/EN60950). In particular, for a non-isolated converter's output voltage to meet SELV (safety extra low voltage) requirements, its input must be SELV compliant. If the output needs to be ELV (extra low voltage), the input must be ELV.

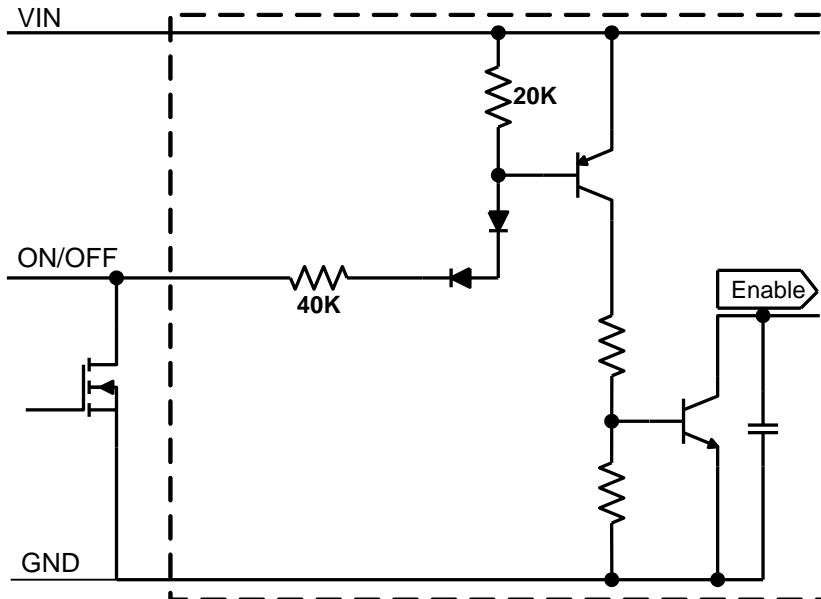
### Remote Sense

MQ7250L03 Power Modules offer a positive output sense function on pin SENSE. The sense function enables point-of-use regulation for overcoming moderate IR drops in conductors and/or cabling. The sense line carries very little current and consequently requires a minimal cross-sectional-area conductor. As such, it is not a low-impedance point and must be treated with care in layout and cabling. Sense lines should be run adjacent to signals (preferably ground). If the remote sense is not needed the sense pin should be left open or connected to V<sub>OUT</sub> directly.

Use of trim and sense functions can cause the output voltage to increase, thereby increasing output power beyond the MQ7250L03's specified rating. Therefore:

$$V_{OUT} \text{ (at pins)} \times I_{OUT} \leq P \text{ (rated output power)}$$

### ON/OFF Control



**Fig5, Remote ON/OFF Implementation with Open Collector/Drain transistor for positive logic control**

The MQ7250L03 power modules feature an On/Off pin for remote On/Off operation with positive logic. If not using the remote On/Off pin, leave the pin open (module will be On). The On/Off pin signal (Von/Off) is referenced to ground. To switch module on and off using remote On/Off, refer to Figure 1a~1b.

### **Output Over voltage Protection**

MQ7250L03 Series products do not incorporate output over voltage protection. If the operating circuit requires protection against abnormal output voltage, voltage-limiting circuitry must be provided external to the power module.

### **Output Overcurrent Protection (OCP)**

MQ7250L03 incorporates overcurrent and short circuit protection. If the load current exceeds the overcurrent protection setpoint, the MQ7250L03's internal overcurrent-protection circuitry immediately turns off the module, which then goes into Hiccup mode. The unit operates normally once the output current is brought back into its specified range. The typical average output current during hiccup is less than 2~3A.

**Caution:** Be careful never to operate MQ7250L03 in a "heavy overload" condition that is between the rated output current and the overcurrent protection setpoint. This can cause permanent damage to the components.

### **Output Voltage Trimming**

MQ7250L03's output voltage can be trimmed in certain ranges. See Performance Specifications for allowable trim ranges in detail. Also customized products are offered.

Trim with external resistor (Fig3), the equation as below:

$$R_{TRIM} = \frac{10 \times 0.8}{V_O - 0.8} - 2.49$$

Resistor values are in kΩ; V<sub>O</sub> is desired output voltage.

For examples, to trim output to 1.5V, then

$$R_{TRIM} = \frac{10 \times 0.8}{1.5 - 0.8} - 2.49 = 8.87$$

So, R<sub>TRIM</sub> = 8.87kΩ

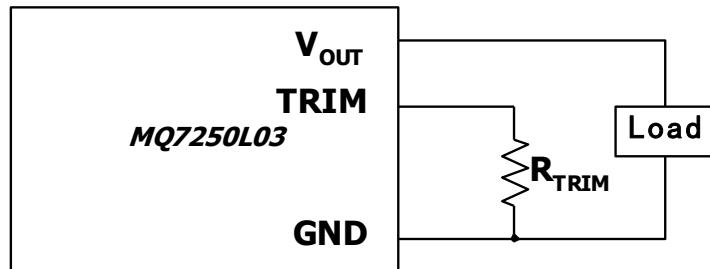


Fig6. Circuit configuration for programming output voltage using external resistor

For most common voltages, the required Trim resistors as Table 2.

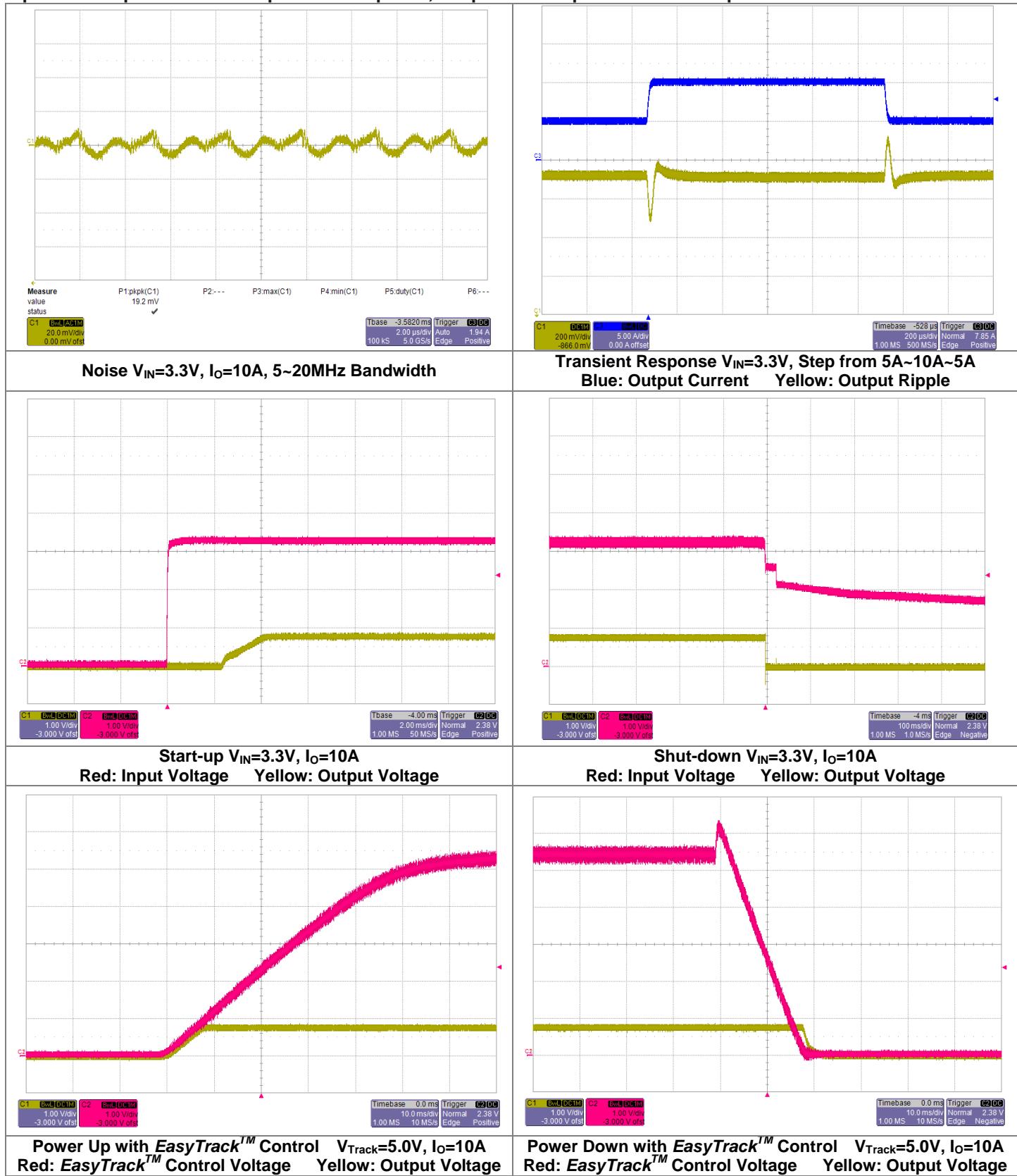
**Table 2, the required trim resistors  $R_{TRIM}$  for most common voltages**

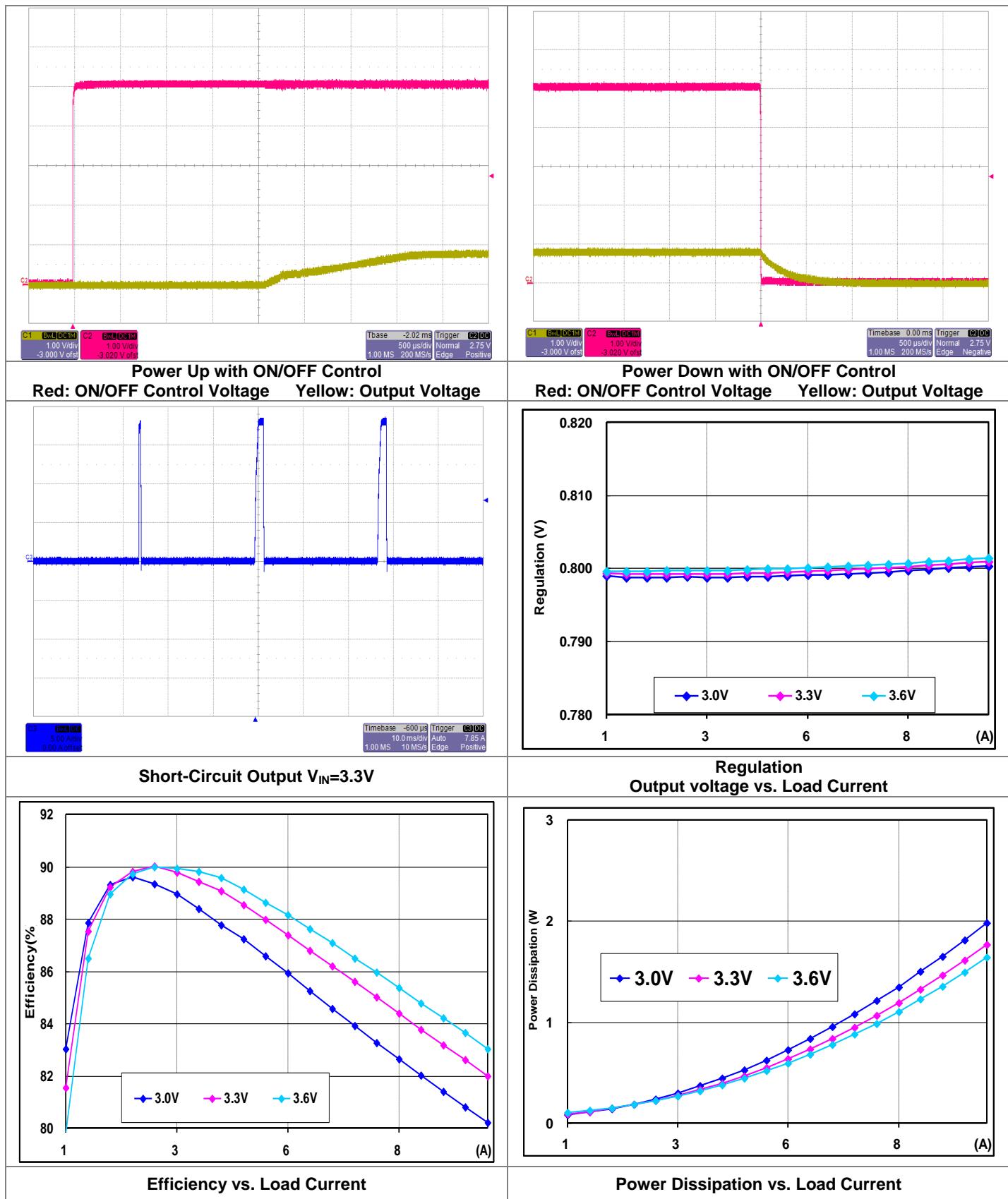
<b>R<sub>TRIM</sub></b>	<b>V<sub>OUT</sub></b>
N/A	0.8V
36.5K	1.0V
17.4K	1.2V
8.87K	1.5V
5.49K	1.8V
2.21K	2.5V

## Typical Characteristics – output adjusted to 0.8V

General conditions:

Input filter 22 $\mu$ F Ceramic + 68 $\mu$ F TAN+470 $\mu$ F AL, Output filter 22 $\mu$ F Ceramic + 100 $\mu$ F TAN

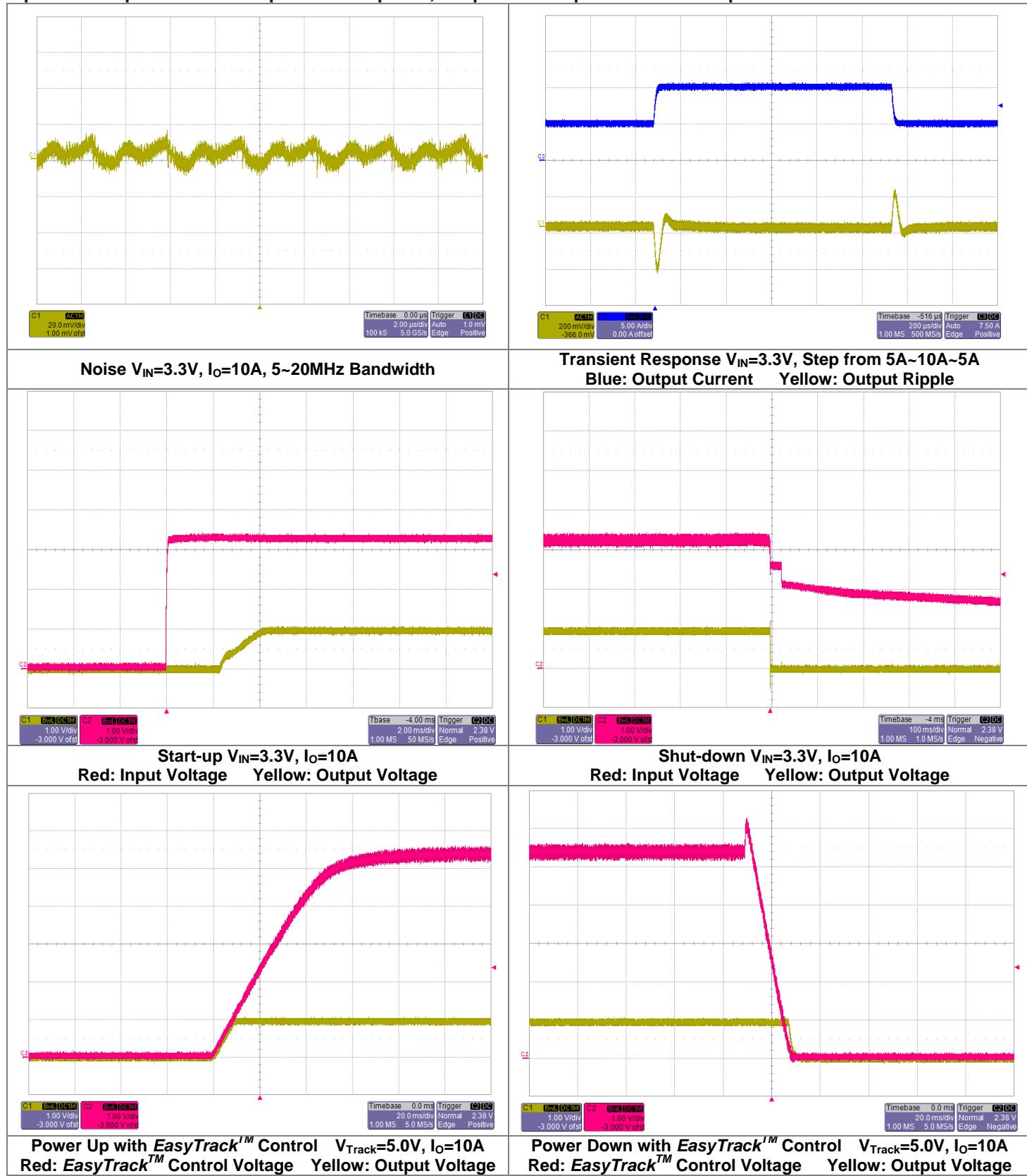


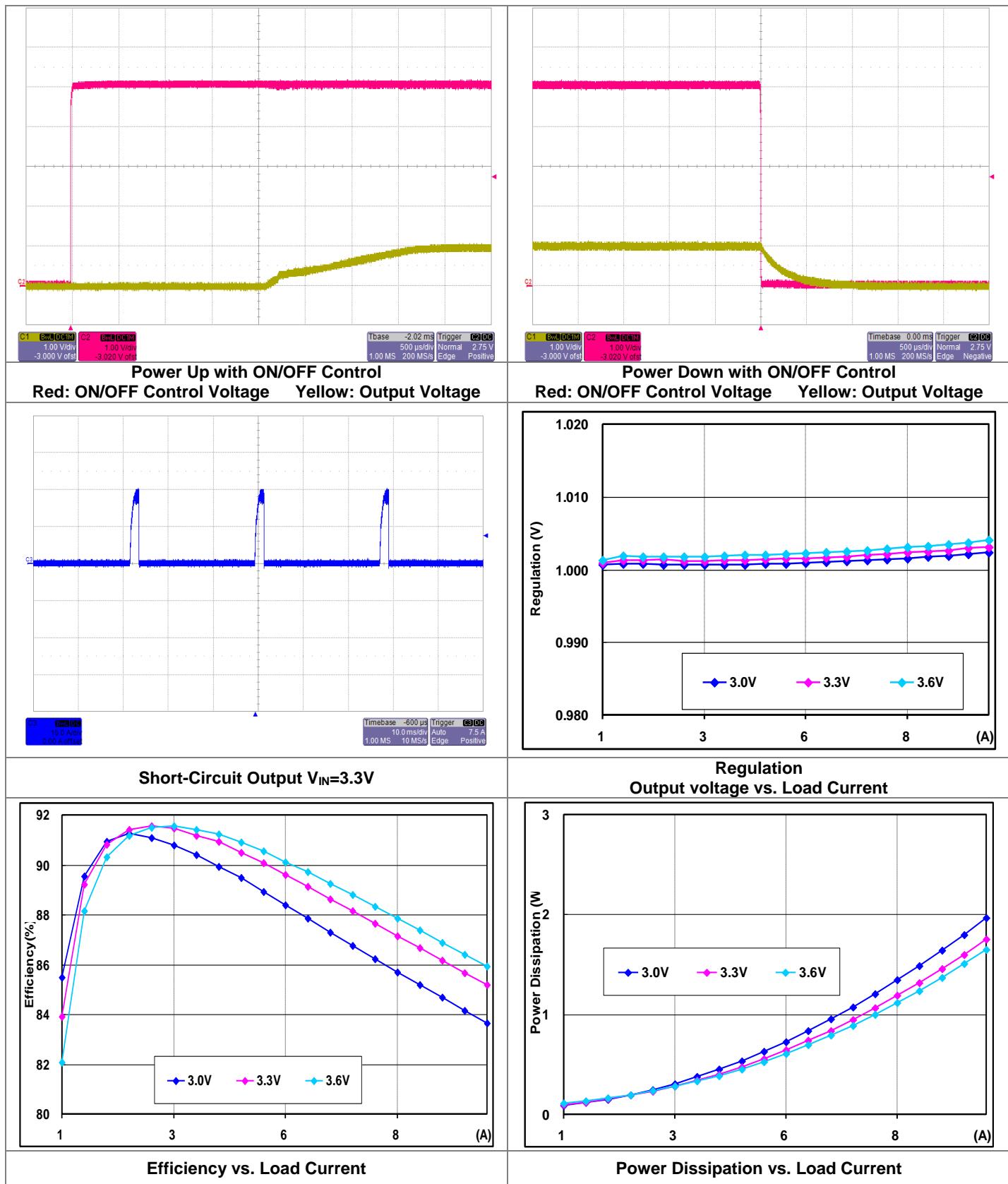


## Typical Characteristics – output adjusted to 1.0V

General conditions:

Input filter 22 $\mu$ F Ceramic + 68 $\mu$ F TAN+470 $\mu$ F AL, Output filter 22 $\mu$ F Ceramic + 100 $\mu$ F TAN

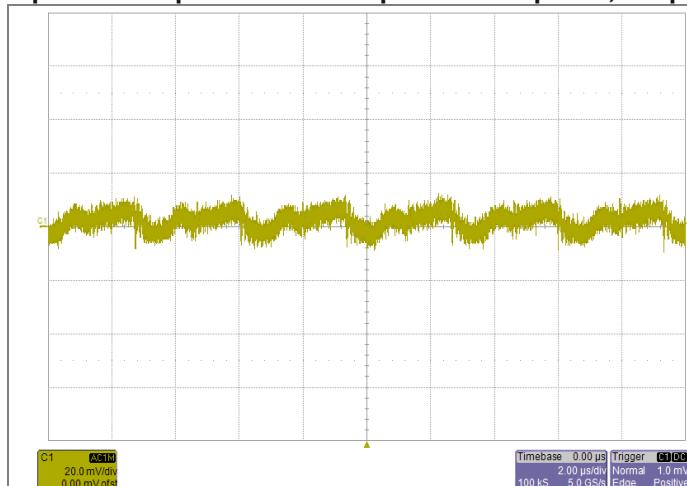




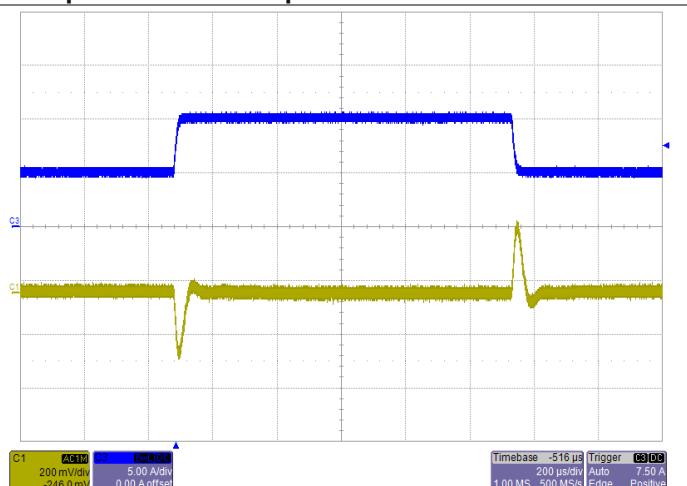
## Typical Characteristics – output adjusted to 1.2V

General conditions:

Input filter 22 $\mu$ F Ceramic + 68 $\mu$ F TAN+470 $\mu$ F AL, Output filter 22 $\mu$ F Ceramic + 100 $\mu$ F TAN



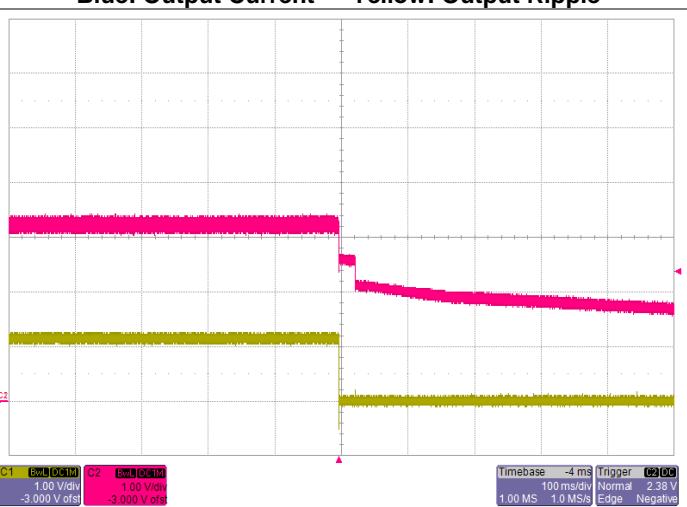
Noise  $V_{IN}=3.3V$ ,  $I_O=10A$ , 5~20MHz Bandwidth



Transient Response  $V_{IN}=3.3V$ , Step from 5A~10A~5A  
Blue: Output Current Yellow: Output Ripple



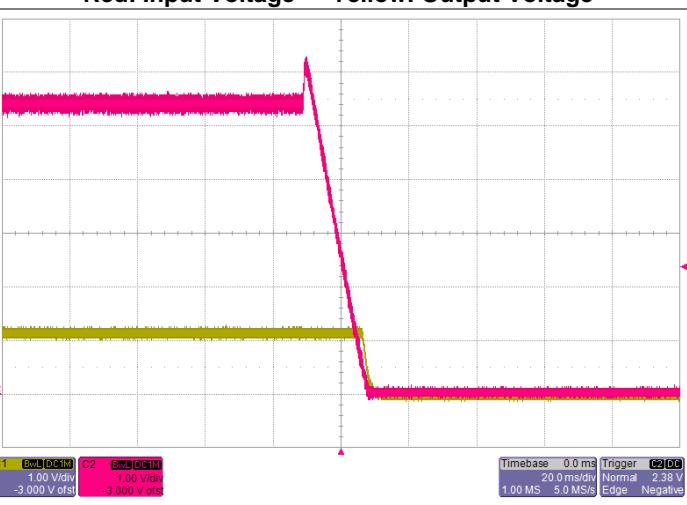
Start-up  $V_{IN}=3.3V$ ,  $I_O=10A$   
Red: Input Voltage Yellow: Output Voltage



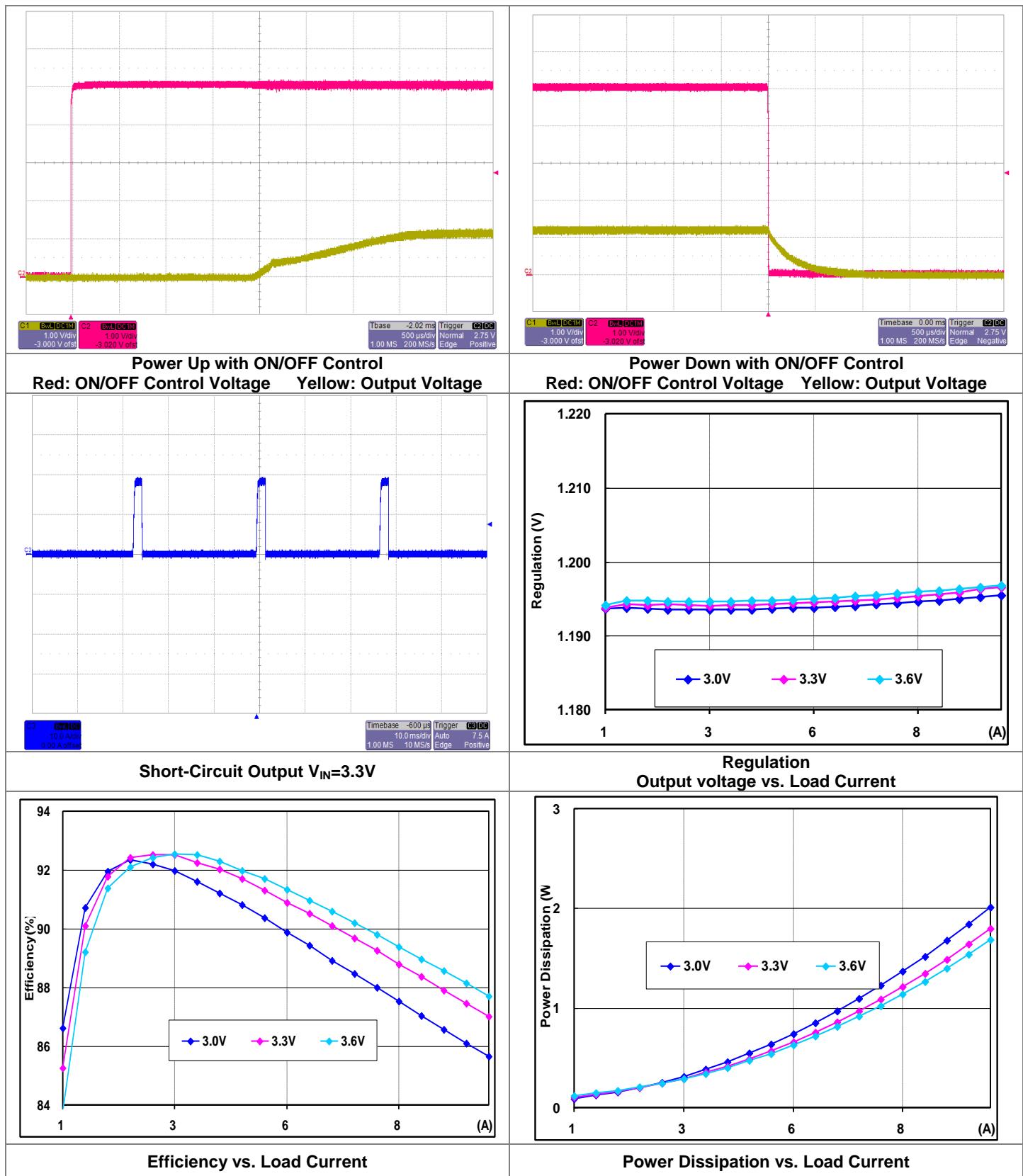
Shut-down  $V_{IN}=3.3V$ ,  $I_O=10A$   
Red: Input Voltage Yellow: Output Voltage



Power Up with EasyTrack™ Control  $V_{TRACK}=5.0V$ ,  $I_O=10A$   
Red: EasyTrack™ Control Voltage Yellow: Output Voltage



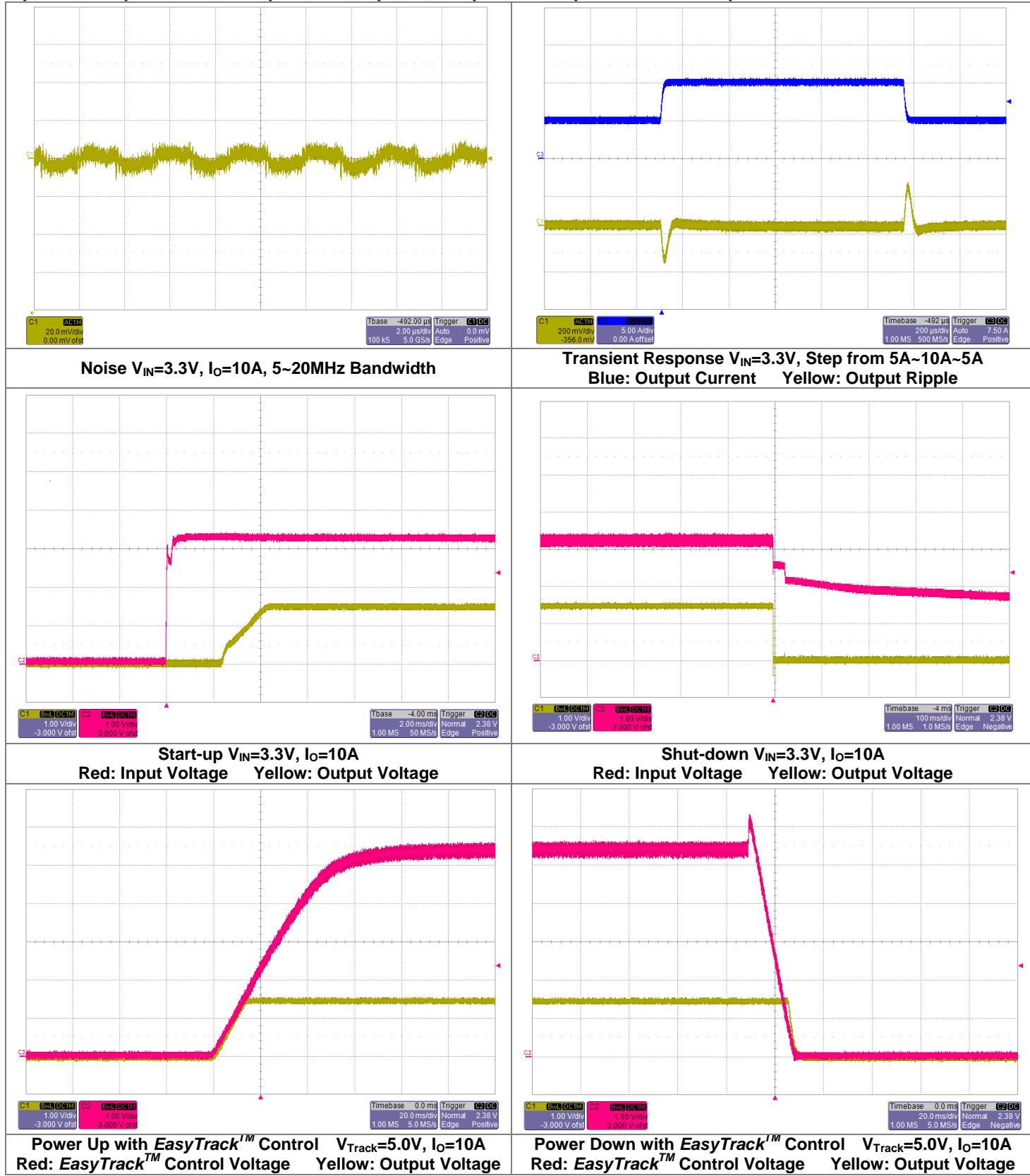
Power Down with EasyTrack™ Control  $V_{TRACK}=5.0V$ ,  $I_O=10A$   
Red: EasyTrack™ Control Voltage Yellow: Output Voltage

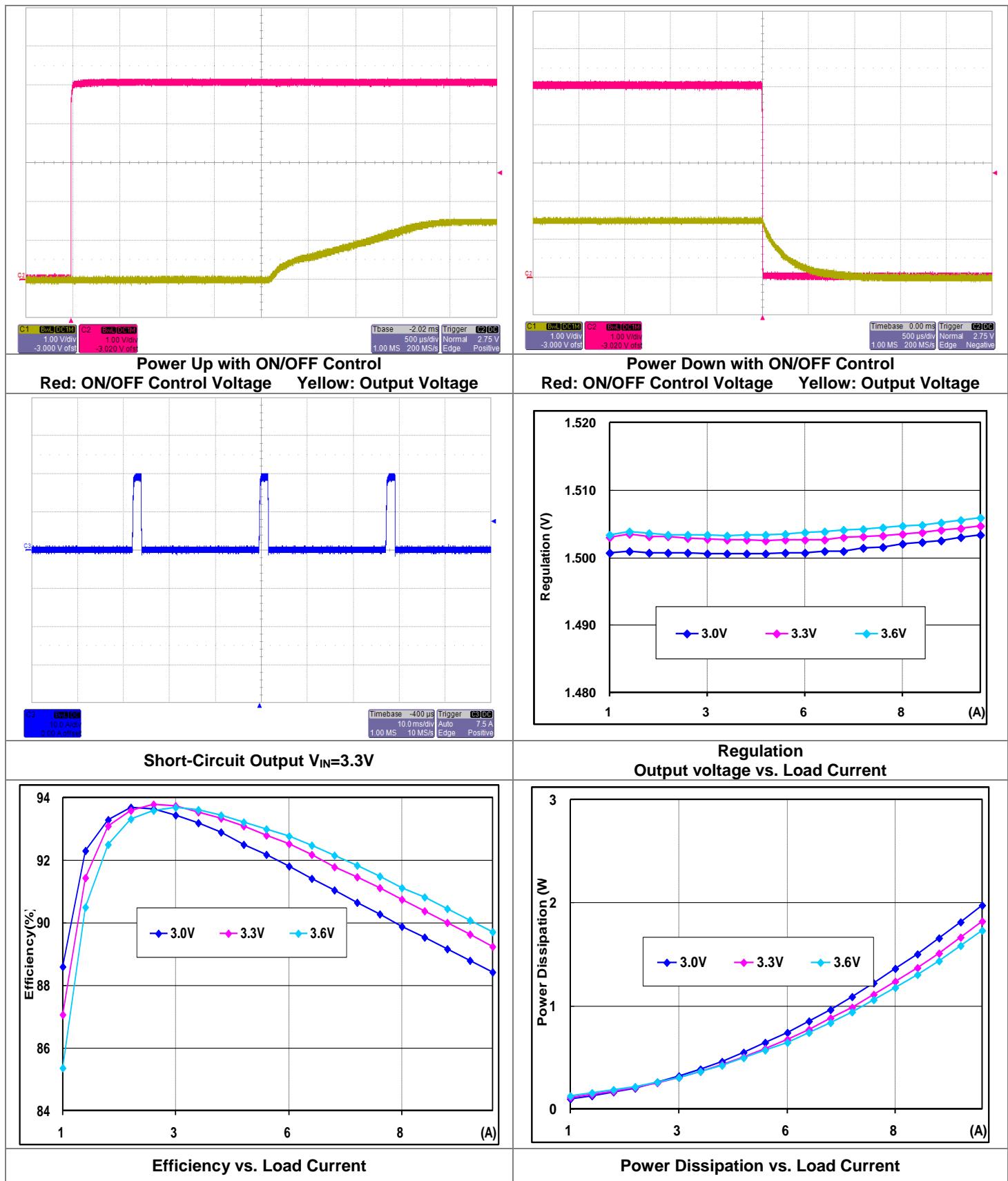


## Typical Characteristics – output adjusted to 1.5V

General conditions:

Input filter 22 $\mu$ F Ceramic + 68 $\mu$ F TAN+470 $\mu$ F AL, Output filter 22 $\mu$ F Ceramic + 100 $\mu$ F TAN

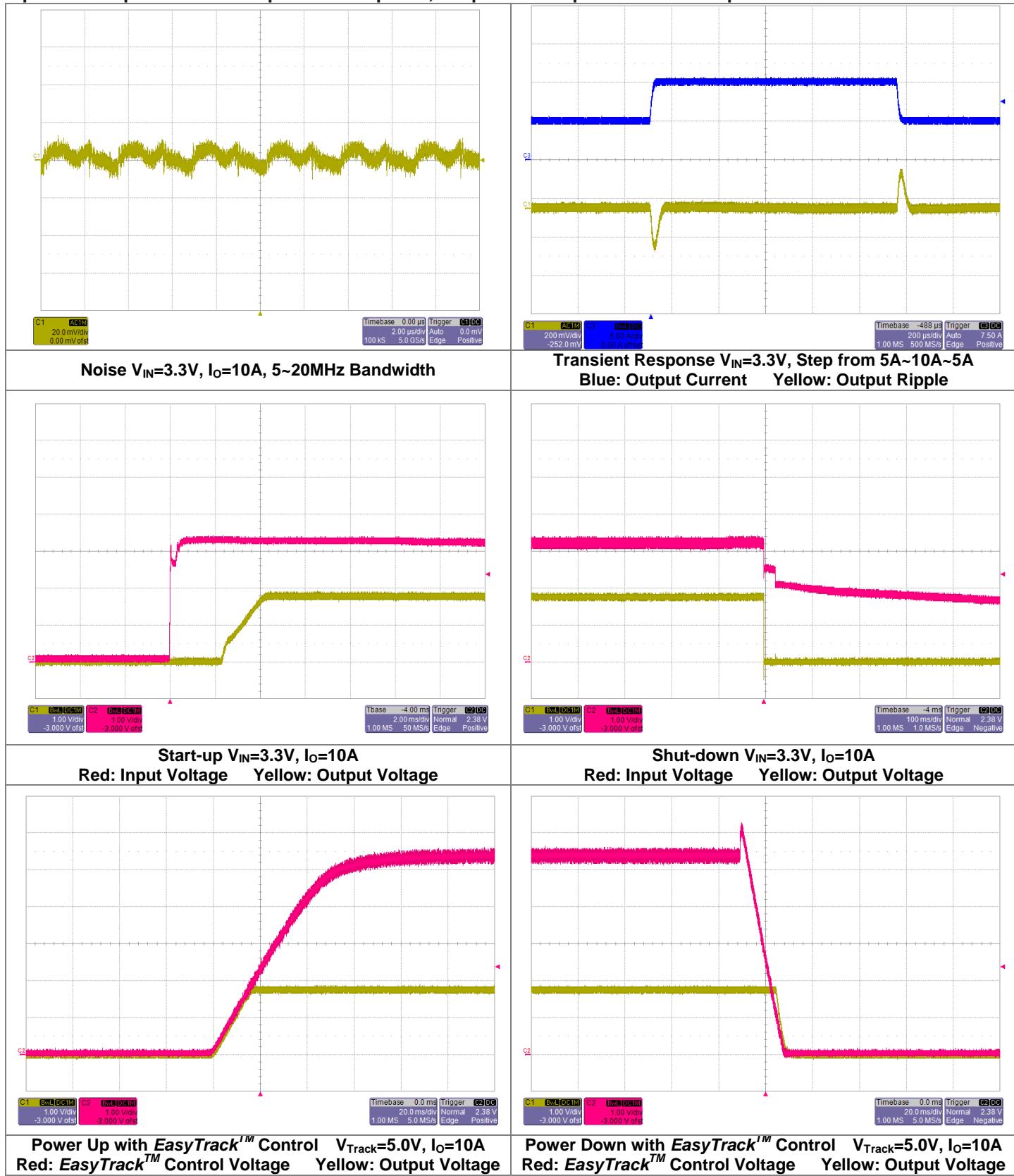


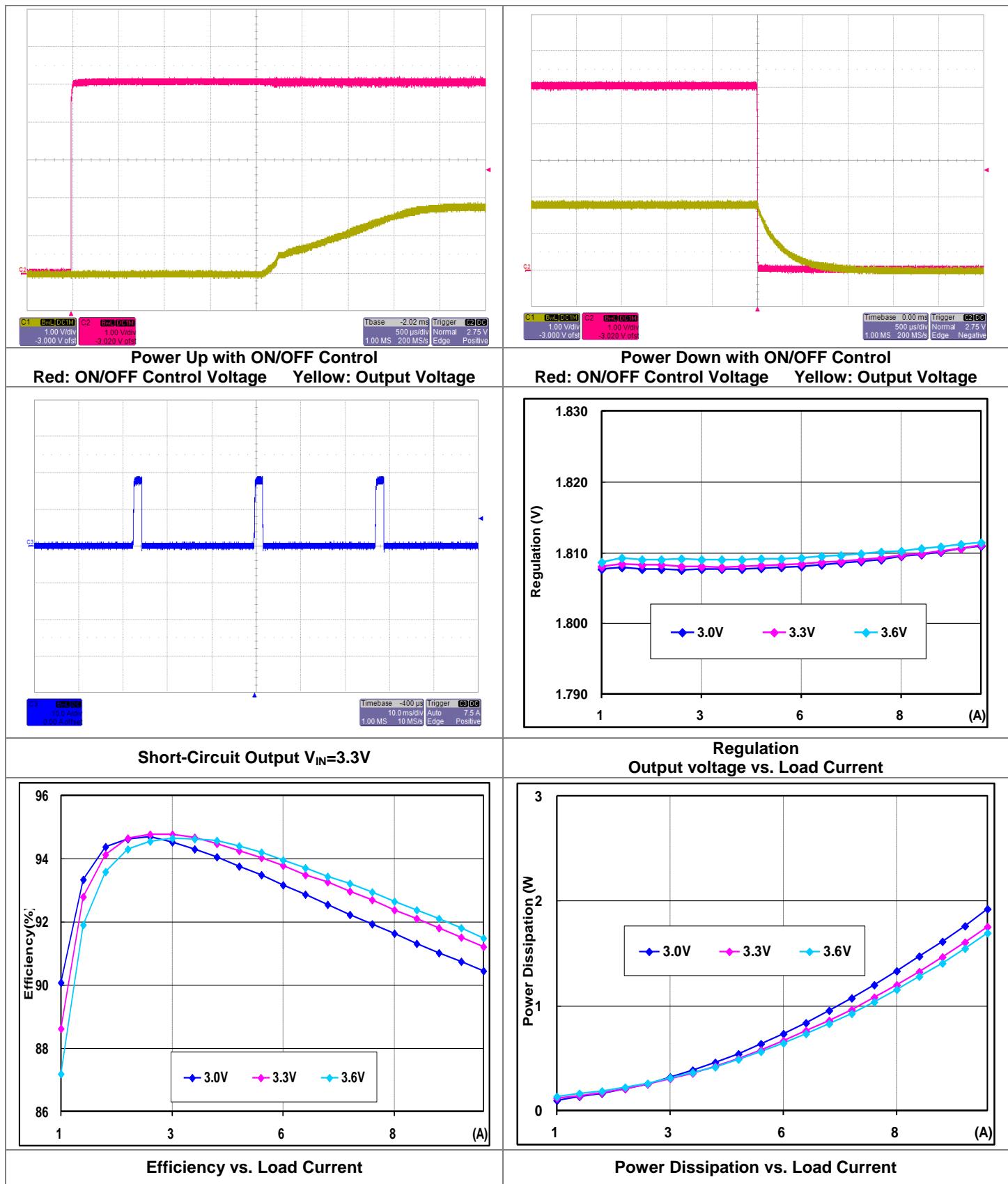


## Typical Characteristics – output adjusted to 1.8V

General conditions:

Input filter 22 $\mu$ F Ceramic + 68 $\mu$ F TAN+470 $\mu$ F AL, Output filter 22 $\mu$ F Ceramic + 100 $\mu$ F TAN

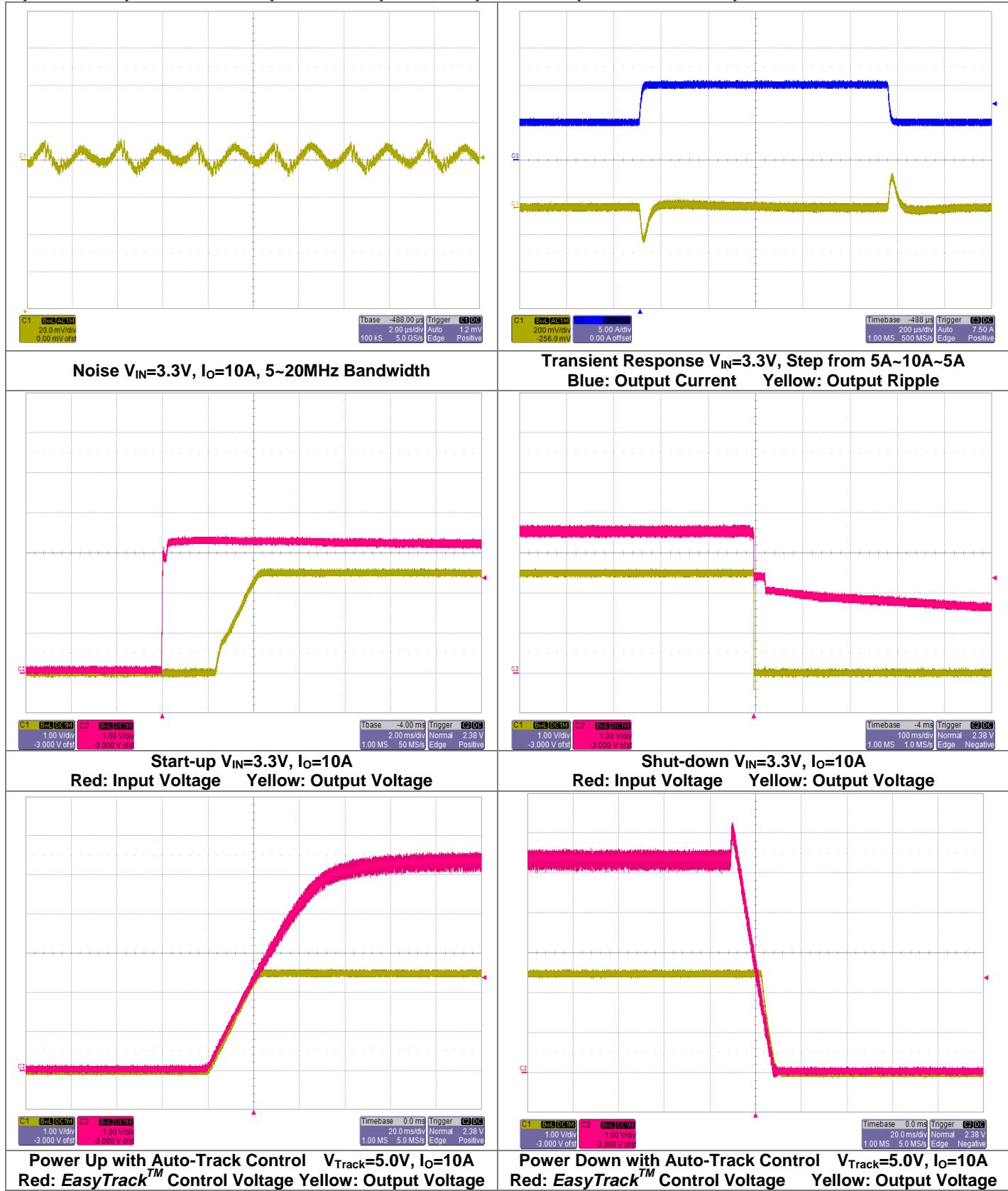


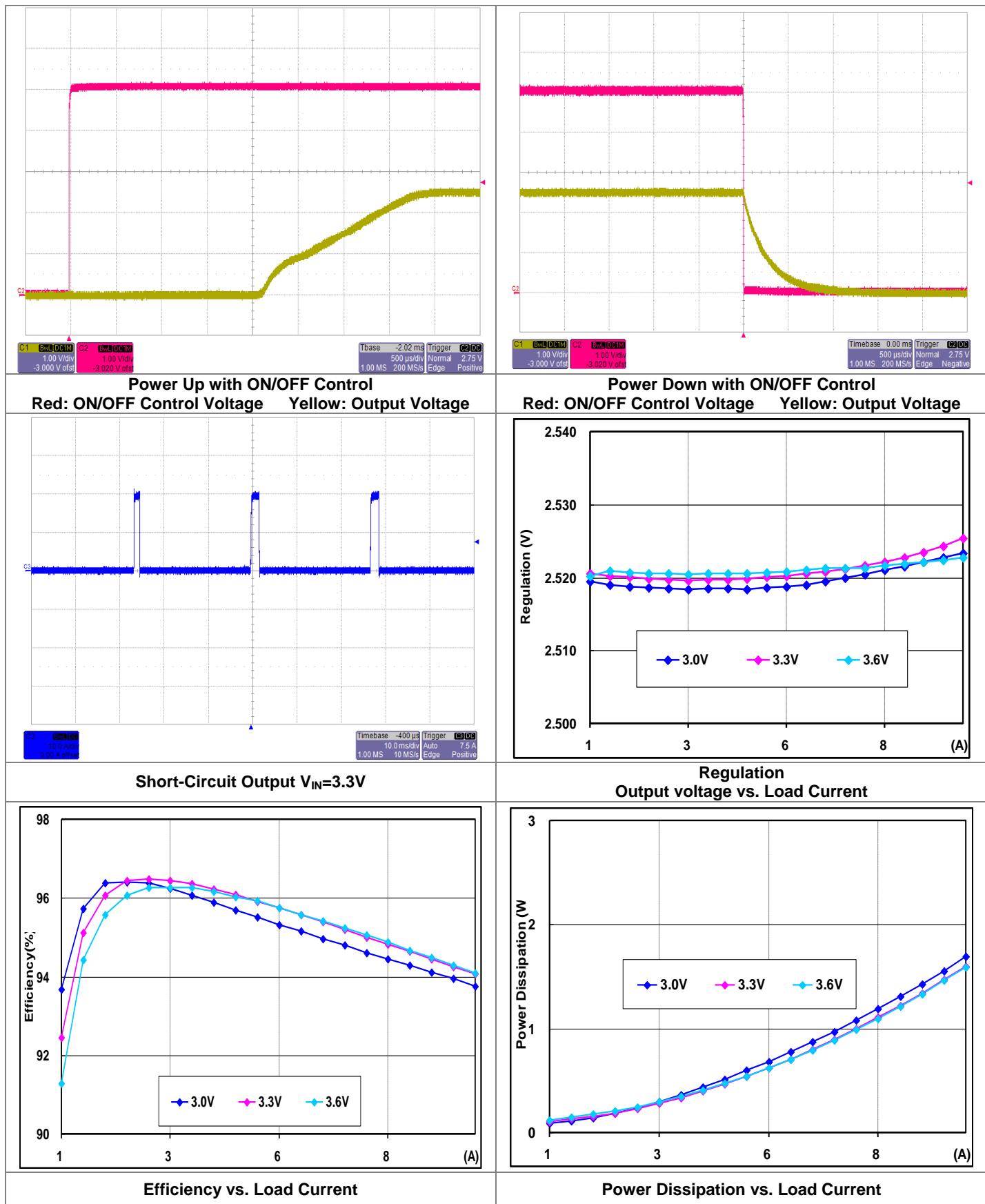


## Typical Characteristics – output adjusted to 2.5V

General conditions:

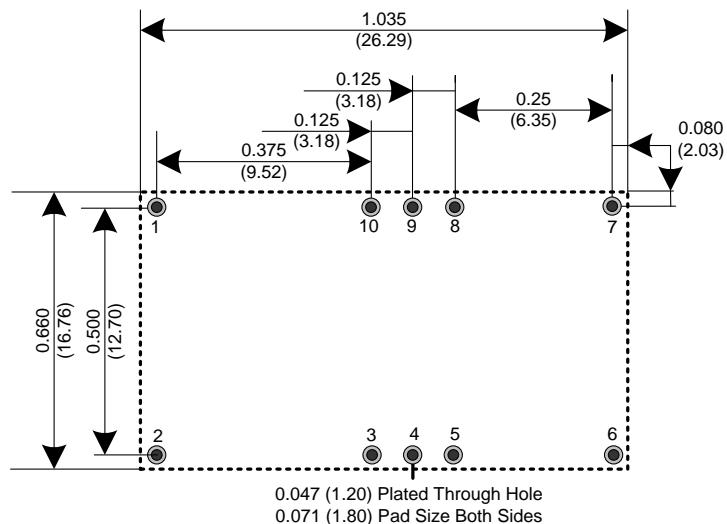
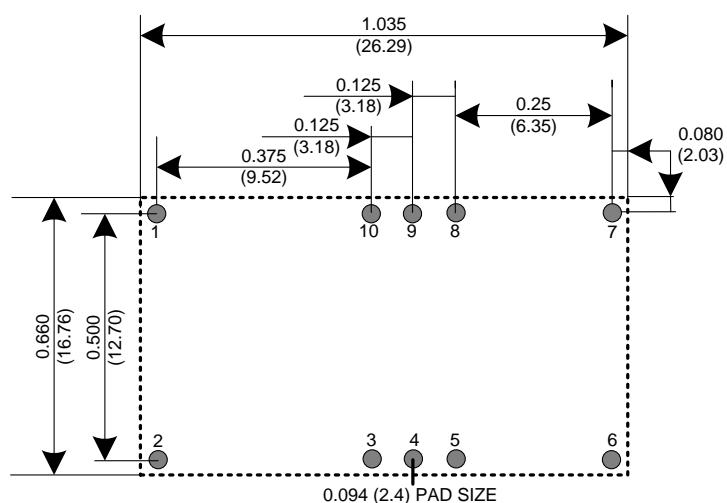
Input filter 22 $\mu$ F Ceramic + 68 $\mu$ F TAN+470 $\mu$ F AL, Output filter 22 $\mu$ F Ceramic + 100 $\mu$ F TAN





**Recommended Pattern for "T" suffix**

Dimensions are in inches (millimeters)

**Recommended Pattern for "S" suffix**



*Application Notes*